

TABLE 2 — SUMMARY OF MC6803E OPERATING MODES

Memory Space Options (64K Address Space)
 Mode 2 — Internal RAM
 Mode 3 — No Internal RAM

TABLE 3 — MODE SELECTION SUMMARY

Mode	P21 PC1	P20 PC0	RAM	Interrupt Vectors	Bus Mode	Operating Mode
3	H	H	E	E	MUX	Multiplexed/No RAM
2	H	L	I	E	MUX	Multiplexed/RAM
1	L	H				Undefined*
0	L	L				Undefined*

Legend:

I — Internal

L — Logic 0

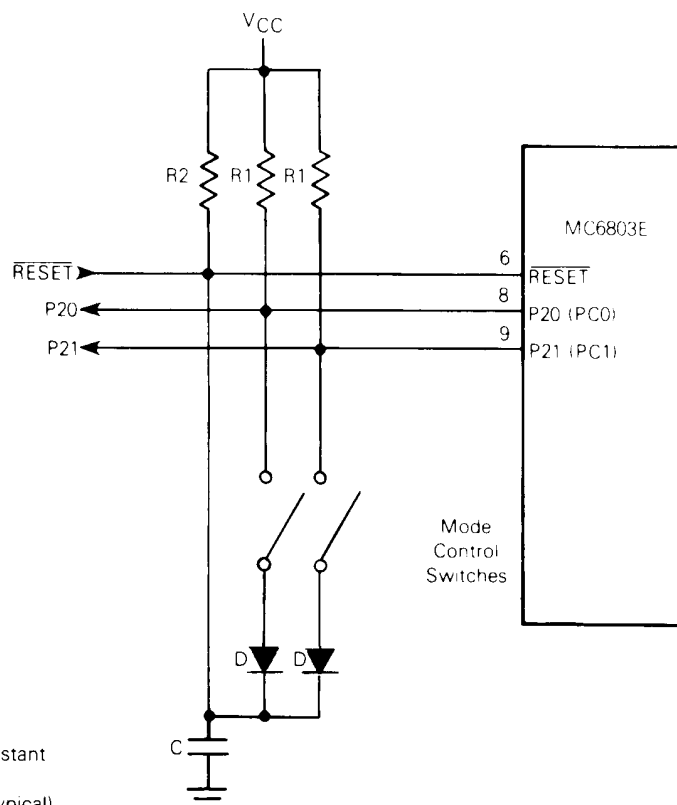
E — External

H — Logic 1

MUX — Multiplexed

* These modes are undefined for the MC6803E; device should not be operated in these modes.

FIGURE 10 — TYPICAL MODE PROGRAMMING CIRCUIT



NOTES:

1. Mode 3 as shown
2. $R2 \cdot C$ = reset time constant
3. $R1 = 10\text{ k}$ (typical)
4. $D = 1\text{N914, } 1\text{N4001}$ (typical)
5. Diode V_f should not exceed V_{MPDD} min.

